

A Modular Approach For High Q Microwave CMOS Active Inductor Design

K.H. Chiang, K.V. Chiang, K.F. Lam, W.W. Choi, K.W. Tam and Rui Martins¹

Faculty of Science & Technology, University of Macau, Macau, China.

Tel: +853 3974369

Fax: +853 838314

Email: fstwwo@umac.mo

ABSTRACT

In this paper, a modular approach for the L-band CMOS Active Inductor (AI) is proposed and designed based upon a connection in series of conventional low Q-factor gyrator-C basic cells. The Q-factor can then be tuned by the number of AI cells in order to offer simplicity in terms of pole/zero cancellation, while maintaining in each gyrator-C inductor cell low values of the Q. In order to demonstrate the proposed architecture usefulness, a prototype L-band active inductor is designed in a 0.6 μ m CMOS process. The simulated results show that a high Q value of 972 is obtained @2GHz and an average Q-factor value of 200 is also achieved in the frequency range of 1-2GHz.

1. INTRODUCTION

Recent inductor integration with other mobile communication functional blocks into a single CMOS chip can obviate the need for external connections, thus avoiding problem of electrical and magnetic coupling, and also pad and bond wire parasitics [1-4]. In addition, this also allows implementing low power and reduced weight mobile communications units. However, the main drawback of the CMOS technology employment in high frequency applications is the low value of the Q factor in the implementation of the inductor. Because of undesirable resonance due to parasitic capacitance and parasitic series resistance in CMOS inductors no matter they are implemented by planar spiral or by simulation, the Q factor value cannot reach values larger than 10 [2]. Recent developments have lead to a significant increase of

interest in Active Inductors (AI) which can be designed based on gyrator type impedance conversion [5]. Using the Active Inductor (AI) approach, the quality factor Q can be increased by employing an active circuit to compensate resistive losses. This paper describes the design and simulated performance of a modular approach for a differential CMOS Active Inductor (AI) based on a typical gyrator-C, which is expected to offer high Q factor.

2. MODULAR ACTIVE INDUCTOR APPROACH

Traditional gyrator-C based active inductor, as depicted in Fig. 1, mainly relies on the use of load capacitances C's and differential transconductances g_{m1} and g_{m2} to simulate the inductance. The input impedance can be represented by the following 4th order function [5-8]:

$$Z_{in}(s) \approx \frac{(s + \omega_{z1})(s + \omega_{z2})(s + \omega_{z3})}{(s + \omega_{p1})(s + \omega_{p2})(s + \omega_{p3})(s + \omega_{p4})} \quad (1)$$

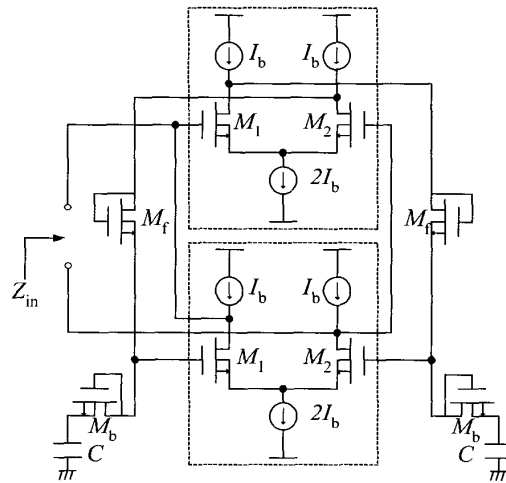


Fig. 1 Gyrator-C based active inductor.

¹ On leave from IST/Lisbon, Portugal

It can be also rewritten in terms of the even (f_{11}, f_{21}) and odd (g_{11}, g_{21}) functions in the numerator and denominator as shown in the following:

$$Z_{ik}(s) \approx \frac{f_{11} + g_{11}}{f_{21} + g_{21}} \quad (2)$$

where

$$\begin{aligned} f_{11} &= s^2(\omega_{Z1} + \omega_{Z2} + \omega_{Z3}) + (\omega_{Z1} + \omega_{Z2} + \omega_{Z3}) \\ g_{11} &= s^3 + s(\omega_{Z1}\omega_{Z2} + \omega_{Z1}\omega_{Z3} + \omega_{Z2}\omega_{Z3}) \\ f_{21} &= s^4 + s^2 \left(\begin{aligned} &\omega_{P1}\omega_{P2} + \omega_{P1}\omega_{P3} + \omega_{P2}\omega_{P3} \\ &+ \omega_{P1}\omega_{P4} + \omega_{P2}\omega_{P4} + \omega_{P3}\omega_{P4} \end{aligned} \right) \\ &\quad + (\omega_{P1}\omega_{P2}\omega_{P3}\omega_{P4}) \\ g_{21} &= s^3(\omega_{P1} + \omega_{P2} + \omega_{P3} + \omega_{P4}) \\ &\quad + s \left(\begin{aligned} &\omega_{P1}\omega_{P2}\omega_{P3} + \omega_{P1}\omega_{P2}\omega_{P4} \\ &+ \omega_{P1}\omega_{P3}\omega_{P4} + \omega_{P2}\omega_{P3}\omega_{P4} \end{aligned} \right) \end{aligned}$$

As the product of two even functions or two odd functions is an even function, whilst the product of an even and an odd function yields odd, the real part and imaginary part of Z_{in} can be then derived:

$$\text{Re} \{Z_{ik}(s)\} = \frac{f_{11}f_{21} - g_{11}g_{21}}{f_{21}^2 - g_{21}^2} \quad (3)$$

$$\text{Im} \{Z_{ik}(s)\} = \frac{f_{21}g_{11} - f_{11}g_{21}}{f_{21}^2 - g_{21}^2} \quad (4)$$

In order to minimize the resistive loss, one can determine one of the high Q conditions

$$\text{Numerator even function } f_{11} = 0 \text{ \&}$$

$$\text{Denominator odd function } g_{21} = 0$$

After some algebraic steps, this leads to an equivalent Z_{in} given by:

$$\begin{aligned} Z'_{in}(s) &= \frac{g_{11}}{f_{21}} \\ &\approx \frac{s^2 + (\omega_{Z1}\omega_{Z2} + \omega_{Z1}\omega_{Z3} + \omega_{Z2}\omega_{Z3})}{s^3 + s \left(\begin{aligned} &\omega_{P1}\omega_{P2} + \omega_{P1}\omega_{P3} + \omega_{P2}\omega_{P3} \\ &+ \omega_{P1}\omega_{P4} + \omega_{P2}\omega_{P4} + \omega_{P3}\omega_{P4} \end{aligned} \right)} \quad (5) \end{aligned}$$

Thus $Z'_{in}(s)$ becomes a third order function through one pole/zero cancellation. This means that a high Q

inductor could be simulated by proper pole/zero cancellation.

In this paper, a modular approach is proposed so as to offer the above cancellation with ease. This structure relies on some reasonable (small) active inductor cells connected in series as shown in Fig. 2.

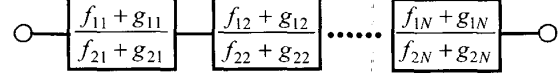


Fig. 2 Modular approach for high Q active inductor.

The resultant input impedance of active inductor of Fig. 2 can be then determined as

$$Z_{in}(s) \approx \sum_{i=1}^N \frac{f_{i1} + g_{i1}}{f_{2i} + g_{2i}} \quad (6)$$

When $N = 2$, the high Q condition becomes

Numerator even function =

$$\begin{aligned} &f_{12}f_{21}(f_{21}f_{22} + g_{21}g_{22}) + f_{11}f_{22}(f_{21}f_{22} + g_{21}g_{22}) \\ &- f_{22}g_{11}(f_{22}g_{21} + f_{21}g_{22}) - f_{21}g_{12}(f_{22}g_{21} + f_{21}g_{22}) \\ &- f_{12}g_{21}(f_{22}g_{21} + f_{21}g_{22}) + g_{12}g_{21}(f_{21}f_{22} + g_{21}g_{22}) \\ &- f_{11}g_{22}(f_{22}g_{21} + f_{21}g_{22}) + g_{11}g_{22}(f_{21}f_{22} + g_{21}g_{22}) = 0 \end{aligned}$$

Denominator odd function =

$$f_{22}g_{21} + f_{21}g_{22} = 0$$

Because the orders of both numerator even function and denominator odd function have been significantly raised, there may exist some frequency ω in the frequency range of concern such that the above condition satisfied. Similarly, we can analyze the cases of three active inductors connected in series and they are:

When $N = 3$, the high Q condition becomes

Numerator even function =

$$\begin{aligned} &f_{13}f_{21}f_{22}(f_3) + f_{12}f_{21}f_{23}(f_3) + f_{11}f_{22}f_{23}(f_3) - f_{22}f_{23}g_{11}(g_3) \\ &- f_{21}f_{23}g_{12}(g_3) - f_{21}f_{22}g_{13}(g_3) - f_{13}f_{22}g_{21}(g_3) - f_{12}f_{23}g_{21}(g_3) \\ &+ f_{23}g_{12}g_{21}(f_3) + f_{22}g_{13}g_{21}(f_3) - f_{13}f_{21}g_{22}(g_3) - f_{11}f_{23}g_{22}(g_3) \\ &+ f_{23}g_{11}g_{22}(f_3) + f_{21}g_{13}g_{22}(f_3) + f_{13}g_{21}g_{22}(f_3) - g_{13}g_{21}g_{22}(g_3) \\ &- f_{12}f_{21}g_{23}(g_3) - f_{11}f_{22}g_{23}(g_3) + f_{22}g_{11}g_{23}(f_3) + f_{21}g_{12}g_{23}(f_3) \\ &+ f_{12}g_{21}g_{23}(f_3) - g_{12}g_{21}g_{23}(g_3) + f_{11}g_{22}g_{23}(f_3) - g_{11}g_{22}g_{23}(g_3) \\ &= 0 \end{aligned}$$

Denominator odd function =

$$\begin{aligned}
 & -f_{13}f_{21}f_{22}(g_3) - f_{12}f_{21}f_{23}(g_3) - f_{11}f_{22}f_{23}(g_3) + f_{22}f_{23}g_{11}(f_3) \\
 & + f_{21}f_{23}g_{12}(f_3) + f_{21}f_{22}g_{13}(f_3) + f_{13}f_{22}g_{21}(f_3) + f_{12}f_{23}g_{21}(f_3) \\
 & - f_{23}g_{12}g_{21}(g_3) - f_{22}g_{13}g_{21}(g_3) + f_{13}f_{21}g_{22}(f_3) + f_{11}f_{23}g_{22}(f_3) \\
 & - f_{23}g_{11}g_{22}(g_3) - f_{21}g_{13}g_{22}(g_3) - f_{13}g_{21}g_{22}(g_3) + g_{13}g_{21}g_{22}(f_3) \\
 & + f_{12}f_{21}g_{23}(f_3) + f_{11}f_{22}g_{23}(f_3) - f_{22}g_{11}g_{23}(g_3) - f_{21}g_{12}g_{23}(g_3) \\
 & - f_{12}g_{21}g_{23}(g_3) + g_{12}g_{21}g_{23}(f_3) - f_{11}g_{22}g_{23}(g_3) + g_{11}g_{22}g_{23}(f_3) \\
 & = 0
 \end{aligned}$$

where

$$\begin{aligned}
 f_3 &= f_{21}f_{22}f_{23} + f_{23}g_{21}g_{22} + f_{22}g_{21}g_{23} + f_{21}g_{22}g_{23} \\
 g_3 &= f_{22}f_{23}g_{21} + f_{21}f_{23}g_{22} + f_{21}f_{22}g_{23} + g_{21}g_{22}g_{23}
 \end{aligned}$$

3. SIMULATION RESULTS

In order to demonstrate the proposed modular structure, 3 active inductors were designed and simulated in 0.6 μ m CMOS technology. The simulation results were obtained by using the SpectreS simulation package of CADENCE. Biased currents of 10mA supplied by conventional current-mirror based current-source are used in these 3 active inductors and the average value of W/L is scaled to 12. The simulated input impedances are shown in Fig. 3 (a) and (b), respectively. It is observed that the Q-factor was increased when an additional active inductor cell is added. In fact, the value of Q can reach 11.8 @2GHz when $N=1$, whilst it is increased to 574 when $N=2$. Specially, the value of Q can reach as high as 972 when $N=3$ @ 2GHz.

In order to show the advantages of using the proposed approach, a detailed analysis of the frequency range between 1.85GHz - 2.003GHz is further investigated in Fig. 4, that shows a substantial decrease of the real part of the input impedance @1.885GHz. The minimum value is closed to 5 Ω @2GHz with an imaginary part as large as 5k Ω . This indeed confirms the contribution of pole/zero cancellation by using this modular series approach.

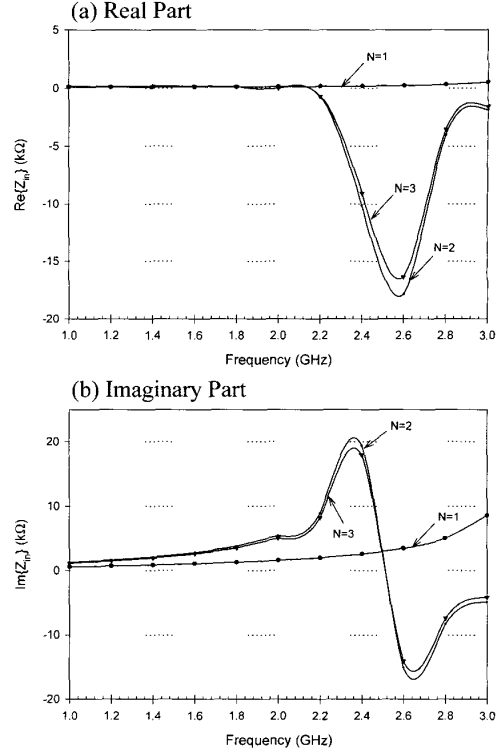


Fig. 3 Simulated CMOS AI impedance (1-3GHz).

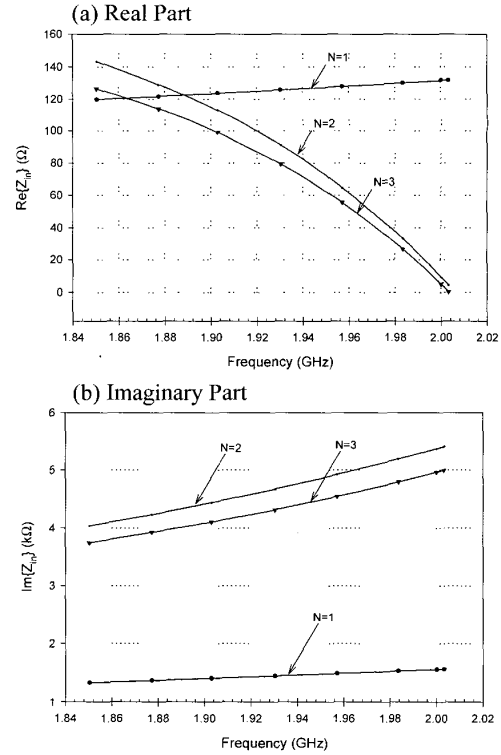


Fig. 4 Simulated CMOS AI impedance (1.885-2.003GHz).

4. CONCLUSIONS

This paper has presented the design of a modular CMOS active inductor in L-band. The simulated results demonstrate that a high Q-factor value of 972 is obtained @2GHz. When the modular series connection changes from $N=1$ to 3, the value of Q-factor can be tuned from 11.8 to 972. It shows that the modular method may offer some pole/zero cancellation in order to tune a high Q-factor. It is envisaged that the application of this CMOS active inductor structure will be a good candidate for the implementations of narrow band-pass filter in mobile communication.

ACKNOWLEDGEMENTS

The authors would like to thank the support of Dr. S. C. Tam and special thank goes also to INESC Macau for the computing tools assistance.

REFERENCES

- [1] Rofougaran A., Chang J., Rofougaran M and Abidi A, "A 1 GHz CMOS RF front-end IC for a direct conversion wireless receiver," *IEEE J. Solid-State Circuits*, SC-31 (7), pp. 880–889, 1996.
- [2] Karanicolas A, "A 2.7V 900 MHz CMOS LNA and mixer," *IEEE J. Solid-State Circuits*, SC-31 (12), pp. 1939–1944, 1996.
- [3] Toumazou C. and Park S.M., "Wideband low-noise CMOS transimpedance amplifier for GHz operation," *Electron. Lett.*, 32 (13), pp. 1194–1195, 1996.
- [4] R.A. Duncan, K.W. Martin and A.S. Sedra, "A Q-enhanced active-RLC bandpass filter," in *Proc. IEEE Int. Symp. On Circuits and Systems*, May 1993, pp. 1416–1419.
- [5] R. Akbari-Dilmaghani, A. Payne and C. Toumazou, "A high Q RF CMOS differential active inductor," in *Proc. 5th IEEE International Conference on Electronics, Circuits and Systems*, vol. 3, Sept., 1998, pp.157-160.
- [6] Y.T. Wang and A. Abidi, "CMOS active filter design at very high frequencies," *IEEE J. Solid-State Circuits*, SC-25 (6), pp. 1562–1574, Dec. 1990.
- [7] Thanachayanont A. and Payne A, "VHF CMOS active inductor," *Electron. Lett.*, 32 (11), pp. 999–1000, 1996.
- [8] Kuhn W., Stephenson W. and Elshabini-Riad A. "A 200 MHz CMOS Q-enhance LC bandpass filter", *IEEE J. Solid-State Circuits*, SC-31 (8), pp. 1112–1121, 1996.